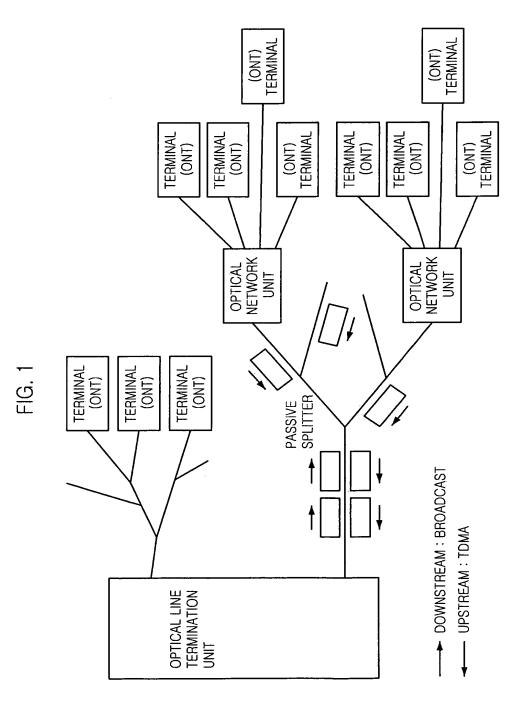


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FIG. 2A

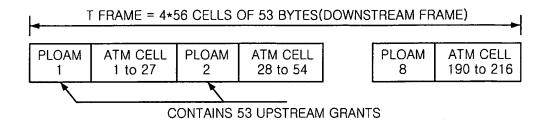
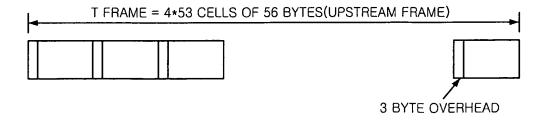
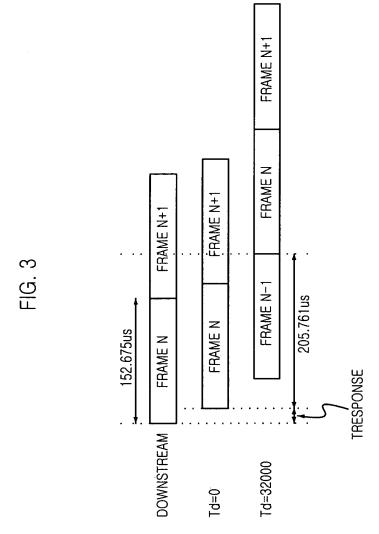
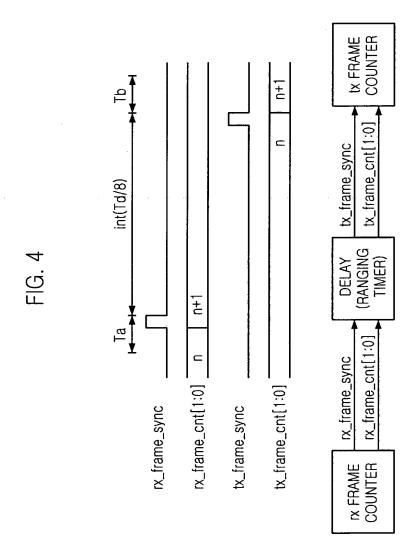


FIG. 2B



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254 546 552 TIMER INTERFACING 551 NUMBER RECEIVER FIG. 5 521 SERIAL 읈 UTOPIA INTERFACING RECEIVING TRANSMITTING UTOPIA S INTERFACING UNI 547 544 SOFTWARE RECEIVING Churning Key Generating Unit BIP ERROR ACCUMULATOR MESSAGE QUEUE STATE MACHINE RANGING PANSMITTING SOFTWARE MESSAGE Queue 512 508 DECHURNING 548/ 545/ N MEMORY ARBITRATING AND INTERFACING UNIT ~543 PROCESSING UNIT MINI-CELL GENERATING UNIT MESSAGE PROCESSING UNIT MESSAGE PROCESSING UNIT **PROCESSING** RECEIVING 100K-UP MESSAGE N N ACKNOWLEDGE TRANSMITTING 542 506 CRC CHECKING UNIT HEADER ERROR 550 **NSPECTING** 541 N 523 505 510 DEMULTIPLEXING TRANSMITTING MULTIPLEXING DECODING UNIT **PECEIVING** 151 GRANT TABLE GRANT N 527 504 FRAME SYNCHRONIZING 524 BIP INSERTING RANGING COUNTER **DUAL-PORT MEMORY** I 503 BIP COMPARING UNIT SCRAMBLER 505 SCRAMBLER 占 TX BYTE CLOCK 526 IX BYTE CLOCK DELAYER 믊 501 DELINEATING CELL AND BYTE Ĭ N 쭖 ≃ 51 52

